

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	("4745305"   "5155065").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/01/12 09:17
L2	11	("6057169").URPN.	USPAT	OR	ON	2006/01/12 09:17
L3	13	L1 L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:23
L4	5872312	cell unit template circuitry	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L5	4462900	input output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L6	338796	(input/output I/o)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L7	4488057	L5 L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L8	274355	L7 adj2 L4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L9	1657863	(( (printed adj circuit PC wiring ) adj (board card module ) ) pcb PWB motherboard mother adj board) carrier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:42
L10	2288538	(microchip micro adj chip chip integrated adj circuit die ic semiconductor adj (device element structure component module) )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L11	128507	L9 with L10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19

L12	8402	L10 with L8	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L13	200	L11 same L12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L14	7365346	connect\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L15	196227	L14 near6 L9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:19
L16	94	L13 and L15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:30
L17	30157	(flip\$3 adj chip flipchip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:32
L18	1935870	(input/output I/o interfac\$4 buffer\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:33
L19	8371216	cell unit template circuit\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:36
L20	3650733	(pad terminal electrode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:38
L21	4149956	(offset\$4 stagger\$3 shift\$3 zigzag\$4 angle\$4 pitch\$4 oblique\$4 displac\$3 )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:39

L22	1657863	(( (printed adj circuit PC wiring ) adj (board card module)) pcb PWB motherboard mother adj board)( ( (printed adj circuit PC wiring ) adj (board card module) ) pcb PWB motherboard mother adj board carrier)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:40
L23	10059364	connect\$4 coupl\$4 ground power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/12 09:42

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	1802	17.clm.	US-PGPUB	OR	ON	2006/01/12 09:43
L25	111771	18.clm.	US-PGPUB	OR	ON	2006/01/12 09:43
L26	350347	19.clm.	US-PGPUB	OR	ON	2006/01/12 09:44
L27	146994	20.clm.	US-PGPUB	OR	ON	2006/01/12 09:47
L28	156628	21.clm.	US-PGPUB	OR	ON	2006/01/12 09:51
L29	78054	22.clm.	US-PGPUB	OR	ON	2006/01/12 09:52
L30	485569	23.clm.	US-PGPUB	OR	ON	2006/01/12 09:53
L31	7	24 and 25 and 26 and 27 and 28 and 29 and 30	US-PGPUB	OR	ON	2006/01/12 09:54